EXTERNAL REFERENCE INPUT

Frequency
5 or 10 MHz, ±300 ppb

Level
+4 dBm ±3dB into 50 ohms

VSWR
1.5:1

Automatic Input Select Level
-1 to +0.5 dBm

OUTPUT

Frequency
10 MHz

Level
+13 dBm ±2 dB into 50 ohms

STABILITY

Aging
1 x 10^{-9} /day after 24 hours operating
5 x 10^{-8}/year, second year, typical

Phase Noise L(f)

10 Hz	-130 dBc/Hz
100 Hz	-155 dBc/Hz
1 KHz	-165 dBc/Hz

Temperature Stability
±1 x 10^{-8}, 0° to +50°C (Ref +25°C)

Harmonics
≤ -30 dBc

Sub-Harmonics
≤ -30 dBc

PLL Divider Products
≤ -80 dBc

Spurious
≤ -80 dBc, excluding power supply line related spurs

Type 2, 3rd order PLL
Detector lock frequency: 2.5 MHz
BW @ 0.1 Hz, nominal
<5 minute to within ±1 x 10^{-9} of input

MECHANICAL

Dimensions
1.75” x 2.938” x 0.6” housing
2.25” x 3.40” footprint with brackets

Connectors
RF Input/Output: SMA(f)
PWR, GND, ET, Status: Feedthru capacitor solder pins

Packaging
Solder sealed steel can

POWER REQUIREMENTS

Electrical Tuning
±1 x 10^{-6}, 0 to +5 VDC
Electrical tuning disengaged when external signal present

Warm-Up Power
≤ 8 Watts for 5 minutes

Total Power
≤ 3.5 Watts at +25°C

Supply Voltage
+15 VDC ±5%

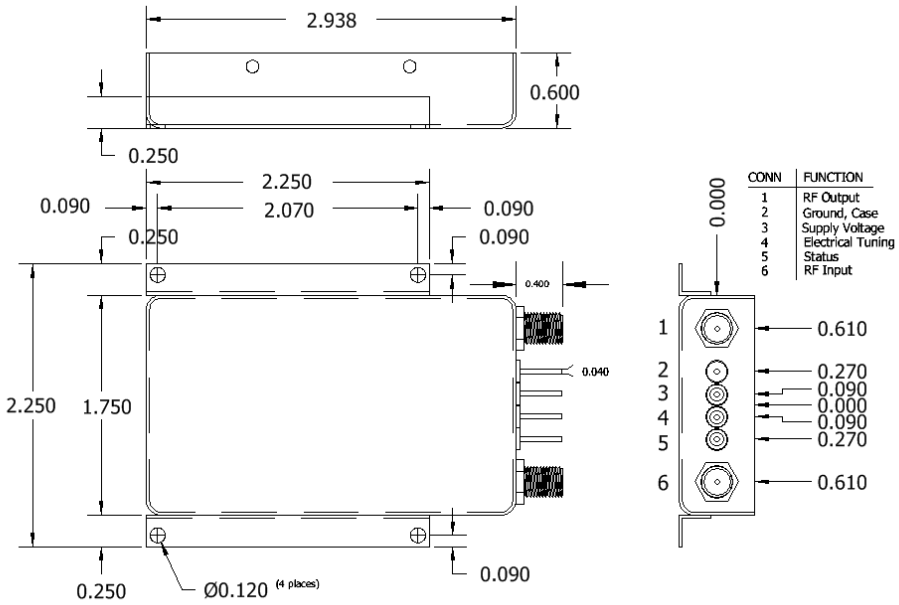
CRYSTAL

Type
10 MHz SC-cut

STATUS PIN

External Reference Loss & Out-of-Lock Alarm
TTL, Low = loss of reference, Not locked
TTL, High = locked

REV	DATE	REVISION RECORD	DWN	AUTH
-	06-01-11	Initial Release	PAC	BB



Wenzel Associates, Inc.
Austin, Texas

Title:
10 MHz-SC Analog Phase Lock Oscillator

P/N:	Rev:	Date:	Drawn:	Ref:
501-24065	**-**	**06-01-11**		17784b

Tolerances: (except as noted) Dimensions are in inches	0.XX Dec:	0.XXX Dec:	FSCM:	Page 1 of 1
	±0.030”	±0.010”	62821	